

### **ABSTRACT OF THE DISCLOSURE**

A clock-synchronizing apparatus and method of devices with different clocks are disclosed. Between a first device operated with a first clock and a second device operated with a second clock faster than the first clock, an operation latency of the second device refers to the first clock, control signals that controls the second device are generated at the second clock speed according to the operation latency, and an enable interval of the control signals has a  $1/4$  clock period of the first clock. Accordingly, since the first device and the second device can transmit and receive a data to and from each other while being operated by using their own clock, an access latency for the first device to access the second device can be reduced and a transmission band width between the two devices can be effectively used.